

4-23-02
Pre Andt A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application Of: Dahl et al.

Serial No. : 09/714,722

Group Art Unit:

Filed : 11/15/2000

Examiner:



: OPTIMIZATION OF ABUTTED-PIN HIERARCHICAL
PHYSICAL DESIGN

PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to the examination of this Patent Application on the merits, please
enter the following preliminary amendment.

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IN THE CLAIMS

Please add the following new Claims 2-52:

2. (New) A method as recited in Claim 1 wherein said physical design
information includes pin assignments of blocks of said prior integrated circuit.

3. (New) A method as recited in Claim 1 wherein said physical design
information includes optimal clock distribution tree of said prior integrated circuit.

04/02/2002 NMOHAMH1 00000111 09714722

01 FC:103 576.00 OP
02 FC:102 84.00 OP

RESH-001/ACM/JSG
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